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VS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. . . . . 09/512,968  
 Filing Date . . . . . February 24, 2000  
 Inventor . . . . . David R. Hembree  
 Assignee . . . . . Micron Technology, Inc.  
 Group Art Unit . . . . . 2832  
 Examiner . . . . . V. Nguyen  
 Attorney's Docket No. . . . . MI22-1363  
 Title: Wafer Processing Apparatuses and Electronic Device Workpiece  
 Processing Apparatuses (As Amended)

**RESPONSE TO JANUARY 29, 2001 OFFICE ACTION**

To: Assistant Commissioner for Patents  
Washington, D.C. 20231

From: James D. Shaurette (Tel. 509-624-4276; Fax 509-838-3424)  
 Wells, St. John, Roberts, Gregory & Matkin P.S.  
 601 W. First Avenue, Suite 1300  
 Spokane, WA 99201-3828

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 MAY - 8 2001  
 TC 800 MAIL ROOM

Sir:

Responsive to the Office Action dated January 31, 2001, Applicant  
 amends and remarks as follows:

05/04/2001 SHINASS1 00000019 09512968

01 FC:103 90.00 OP  
 01 FC:102 640.00 OP

**AMENDMENTS**

**In the Title**

Please replace the title with the following: --WAFER PROCESSING  
 APPARATUSES AND ELECTRONIC DEVICE WORKPIECE PROCESSING  
 APPARATUSES--.